Substitute form 1449/PTO				Complete if Known			
				Application Number	new	o.	
INFO	RMATION DIS	CL O	SURF	Filing Date	September 22, 2000	202	
STATEMENT BY APPLICANT				First Named Inventor	Charles Jay Alpert et al	.s. P.	
				Group Art Unit	n/a	12 d	
				Examiner Name	n/a	C5.	
Sheet	1	of	2	Attorney Docket Number	AUS920000349US1		

100/25/60 100/25/60

U.S. PATENT DOCUMENTS					
Examiner Initials	IDS Doc#	U.S. Patent Document Number	Name of Patentee or Applicant	Date of Publication MM-DD-YYYY	

FOREIGN PATENT DOCUMENTS						
Examiner Initials	IDS Doc#	Office	Foreign Patent Document Number	Name of Patentee or Applicant	Date of Publication MM-DD-YYYY	English Translation

		OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS	
Examiner Initials	IDS Doc#	Include the name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	English Translation
melt	AA	B. TUTUIANU, F. DARTU, and L. PILEGGI; "An Explicit RC-Circuit Delay Approximation Based on the First Three Moments of the Impulse Response"; IEEE/ACM Design Automation Conference; 1996; pp. 611-616;Las Vegas, NV, USA	
mey	АВ	P.R. O'BRIEN, and T.L. SAVARINO; "Modeling the Driving-Point Characteristic of Resistive Interconnect for Accurate Delay Estimation"; IEEE, 1989; pp. 512-515.	
mey	AC	J. QIAN, S. PULLELA, and L. PILLAGE; "Modeling the 'Effective Capacitance' for the RC Interconnect of CMOS Gates"; <i>IEEE Transaction on Computer-Aided Design of Integrated Circuits and Systems</i> , 1994, pp. 1526-1535, Vol. 13, No. 12.	
meh	AD	R. KAY, and L. PILEGGI; "PRIMO: Probability Interpretation of Moments for Delay Calculation"; Carnegie Mellon University, Dept. of ECE, Pittsburgh PA., pp. 1-20.	
met	AE	L.T. PILLAGE, and R.A. ROHRER; "Asymptotic Waveform Evaluation for Timing Analysis"; <i>IEEE Transaction on Computer-Aided Design</i> , 1990, pp. 352-366, Vol. 9, No. 4.	
mey	AF	A.B. KAHNG, and S. MUDDU; "An Analytical Delay Model for RLC Interconnects"; IEEE Transaction on Computer-Aided Design of Integrated Circuits and Systems"; 1997, pp. 1507-1514, Vol. 16, No. 12.	
met	AG	C.J. ALPERT, A. DEVGAN, and C. KASHYAP; "A Two Moment RC Delay Metric for Performance Optimization"; 2000.	
mest	АН	W.C. ELMORE; "The Transient Response of Damped Linear Networks with Particular Regard to Wideband Amplifiers"; <i>University of California, Los Alamos Scientific Laboratory, Santa Fe, New Mexico</i> ; 1948, pp. 55-63, Vol. 19, January 1948.	

L. PILEGGI; "Timing Metrics for Physical Design of Deep Submicron Technologies"; Department of Electrical and Computer Engineering, Carnegie Mellon University, Monterey, CA, 1998, pp. 28-33 J. RUBINSTEIN, P. PENFIELD, JR., and M.A. HOROWITZ; "Signal Delay in RC Tree Networks"; IEEE, 1983, pp. 202-210.	
Networks"; IEEE, 1983, pp. 202-210.	
P. CLIDTA P. KRALITER R. TUTLIJANIL I MILLIS and I. T. DILECCU The States	
R. GUPTA, B. KRAUTER, B. TUTUIANU, J. WILLIS and L.T. PILEGGI; "The Elmore Delay as a Bound for RC Trees with Generalized Input Signals"; <i>The University of Texas at Austin, Department of Electrical and Computer Engineering, Austin, Texas</i> ; 32 rd Design Automation Conference, 1995, pp. 364-369.	
T. LIN, E. ACAR, and L. PILEGGI; "H-GAMMA: An RC Delay Metric Based on a Gamma Distribution Approximation of the Homogeneous Response"; pp. 1-8.	
	Texas at Austin, Department of Electrical and Computer Engineering, Austin, Texas; 32 rd Design Automation Conference, 1995, pp. 364-369. T. LIN, E. ACAR, and L. PILEGGI; "H-GAMMA: An RC Delay Metric Based on a

Examiner Signature	Date Considered	08/12,2004
-----------------------	--------------------	------------

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.